

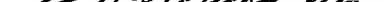


<p>REMARKS</p> <p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>				<p>Complete If Known</p>	
Sheet	1	of		Application Number	09/945,266
				Filing Date	August 31, 2001
				First Named Inventor	Richard Coulson
				Art Unit	2186
				Examiner Name	W. Choi
				Attorney Docket Number	42390P11446

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.*	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document *	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ^a (if known)			
WAC		US-4430712	02-07-1984	COULSON et al	
		US-4468730	08-28-1984	DODD et al	
		US-4503501	03-05-1985	COULSON et al	
		US-4536836	08-20-1985	DODD et al	
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		US-5787296	07-28-1998	GRIMSRUD et al	
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		US-6370614		TEOMAN et al	
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		US-6072490		BATES et al	
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		US-5499337	03-12-1996	GORDON	

FOREIGN PATENT DOCUMENTS

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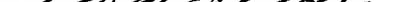
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		Number - Kind Code ³ (if known)			
WUC	US-3353430		11-21-1967	BRACKMAN et al	
	US-4908793		03-13-1990	YAMAGATA et al	
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	US-5542066		07-30-1996	MATTSON et al	
	US-5586291		12-17-1996	LASKER et al	
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
W.C.		COULSON - US PATENT APPLICATION 09/602,008 "IN-LINE CACHE"	
		COULSON - US PATENT APPLICATION 09/602,009 "NON-VOLATILE CACHE EXPANSION BOARD"	
		COULSON - US PATENT APPLICATION 09/602,010 "NON-VOLATILE CACHE"	
		COULSON - US PATENT APPLICATION 09/602,011 "NON-VOLATILE CACHE INTEGRATED WITH MASS STORAGE DEVICE"	
		COULSON - US PATENT APPLICATION 10/740,633 "NON-VOLATILE MASS STORAGE CACHE COHERENCY APPARATUS ADMINISTRATOR"	
		AUCIELLO et al - THE PHYSICS OF FERROELECTRIC MEMORIES, Physics Today, July 1998, pp 22-27	
		SGS THOMSON - GSF32-16x16/90 32 MByte SIMM Glash Memory Module 4 pages	
		DERWENT - DOCUMENT ID RD 431135 A - "A raid controller with a removable NVRAM cache on detecting a battery or an NVRAM fault automatically flushes all...3 pages	
		DEROWITSCH - Smart Computing - "The Quiet Role of Chipsets" Aug. 1998, Vol 9, Issue 8	
		DIEFENDORFF - Intel Tries Integrating Graphics - In-Star MDR Aug. 24, 1998	
		DPT - Smart IV PM2144W PCI High Performance SCSI Adapter	
		DPT - News Release - DPT's SmartCache IV RAID/Caching Kits Outperform Adaptec's SCSI Adapters in Independent Tests and Reviews - Aug. 1, 1996	
		HODGES et al - 1983, pp 388-389 - Analysis and Design of Digital Integrated Circuits	
		INTEL - June 1997 - Intel 430HX PCIset Design Guide pp 1-1 thru 6-15	

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wlc		INTEL - April 1997 - 82371FB (PIIX) AND 82371SB (PIX3) PCI ISA IDE XCELERATOR	
		IRE TRANSACTIONS - COMPONENT PARTS - Switching the Memory Matrix	
		MERZ - Sept. 1955 - Ferroelectric Storage Devices pp 335-342	
		MICROSOFT PRESS - Computer Dictionary 2nd Ed. 1994 - Nondestructive readout, page 270	
		MOAZZAMI et al - Oct 1990, Vol. 11, Issue 10 Citation and Abstract IEEE/IEE Electronic Library - A ferroelectric DRAM cell for high-density NVRAMs	
		MUELLER - 2002 - UPGRADING AND REPAIRING PCS - pages 363-366 Chapter 9 - Chipset Evolution	
		NTIS - Dept of Navy - Jan 14, 1974 - MULTI-WIRE CABLE TO COAXIAL CABLE TRANSITION APPARATUS - ADD000450, 8 pages	
		IBM Technical Disclosure Bulletin, Jul. 1982 - OPTICAL/MAGNETIC STORAGE DISK SYSTEM, Vol 25, Issue 2 pages 459-460	
		PULVARI - Mar. 1956, pp 3-11 - FERROELECTRICS AND THEIR MEMORY APPLICATIONS	
		RAMTRON Dec. 7, 1999 - FM24C64 64Kb FRAM Serial Memory - 15 pages	
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↓		ROSENBERG - 1987 - Dictionary of Computers, Information Processing & Telecommunications, 2nd Ed. - P. 221	

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<i>whic</i>		SUN MICROSYSTEMS - White Paper - Jun 29, 1999 - Sun StorEdge A5200 Fibre Channel Array and Sun Enterprise 10000 Server Set New Milestones for I/O Speed and performance
		TANZAWA - May 1, 1997 IEEE JOURNAL OF SOLID STATE CIRCUITS, IEEE - Vol 32, No 5, pp 662-668 - A COMPACT ON-CHIP ECC FOR LOW COST FLASH MEMORIES
		WEBSTER'S 9th New Collegiate Dictionary 1986 page 733 - entry for matrix
		WHITE 1993 Pages 71-73 - How Computers Work, Chapter 11 - HOW A DISK CACHE WORKS

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